

(SCHEME OF EXAMINATIONS)

Scheme of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) SchemeID: 310522015001 Sem./Year: 01 SEMESTER
 Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Paper ID	Code	Subject	Credit	Type	Exam	Mode	Kind	Minor	Major	Max. Marks	Pass Marks
01	52601	MEVS601	DIGITAL SYSTEM DESIGN USING VERILOG	4	THEORY	UES	COMPULSORY	DROPPABLE	25	75	100	40
02	52603	MEVS603	VLSI TECHNOLOGY	4	THEORY	UES	COMPULSORY	DROPPABLE	25	75	100	40
03	52605	MEVS605	ADVANCED VLSI DESIGN	4	THEORY	UES	COMPULSORY	DROPPABLE	25	75	100	40
04	48607	MECS607	ADVANCED COMPUTER ARCHITECTURE	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
05	52609	MEVS609	ALGORITHM ANALYSIS AND DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
06	48611	MECS611	COMPUTATIONAL TECHNIQUES USING MATLAB	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
07	52611	MEVS611	DSP FOR VLSI DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
08	52613	MEVS613	WIRELESS NETWORKS	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
09	52615	MEVS615	RESEARCH METHODOLOGIES	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
10	52651	MEVS651	LAB-I (DSD LAB)	1	PRACTICAL	UES	COMPULSORY	DROPPABLE	40	60	100	40
11	52653	MEVS653	LAB-II (VLSI TECH. LAB)	1	PRACTICAL	UES	COMPULSORY	DROPPABLE	40	60	100	40
12	52655	MEVS655	LAB-III (AD. VLSI LAB)	1	PRACTICAL	UES	COMPULSORY	DROPPABLE	40	60	100	40
13	52657	MEVS657	TERM PAPER-I	2	PRACTICAL	NUES	COMPULSORY	DROPPABLE	--	100	100	40

